

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,015,559 B2
APPLICATION NO. : 10/652066
DATED : March 21, 2006
INVENTOR(S) : Tim Murphy and Lee Gotcher

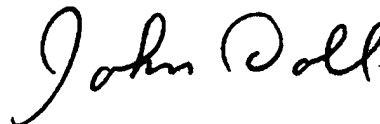
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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

<u>Column, Line</u>	<u>Reads</u>	<u>Should Read</u>
Column 5, Lines 16-17 and 19	"electromagnetic signals 107,105,104"	--electromagnetic signals 107, 105, 104--
Column 5, Line 52	"signals 131, and"	--signals 131 and--
Column 6, Lines 34-36	"a memory-cell array coupled to the address decoder, control circuit, and read/write circuit, the memory-cell array; and"	--a memory cell array coupled to the address decoder, control circuit, and read/write circuit; and--
Column 7, Line 2	"converter mounted on the chip"	--converter is mounted on the chip--

Signed and Sealed this

Third Day of February, 2009



JOHN DOLL
Acting Director of the United States Patent and Trademark Office